

PATENT APPLICATION

RECEIVE BAND REJECTION FOR A DIGITAL RF AMPLIFIER

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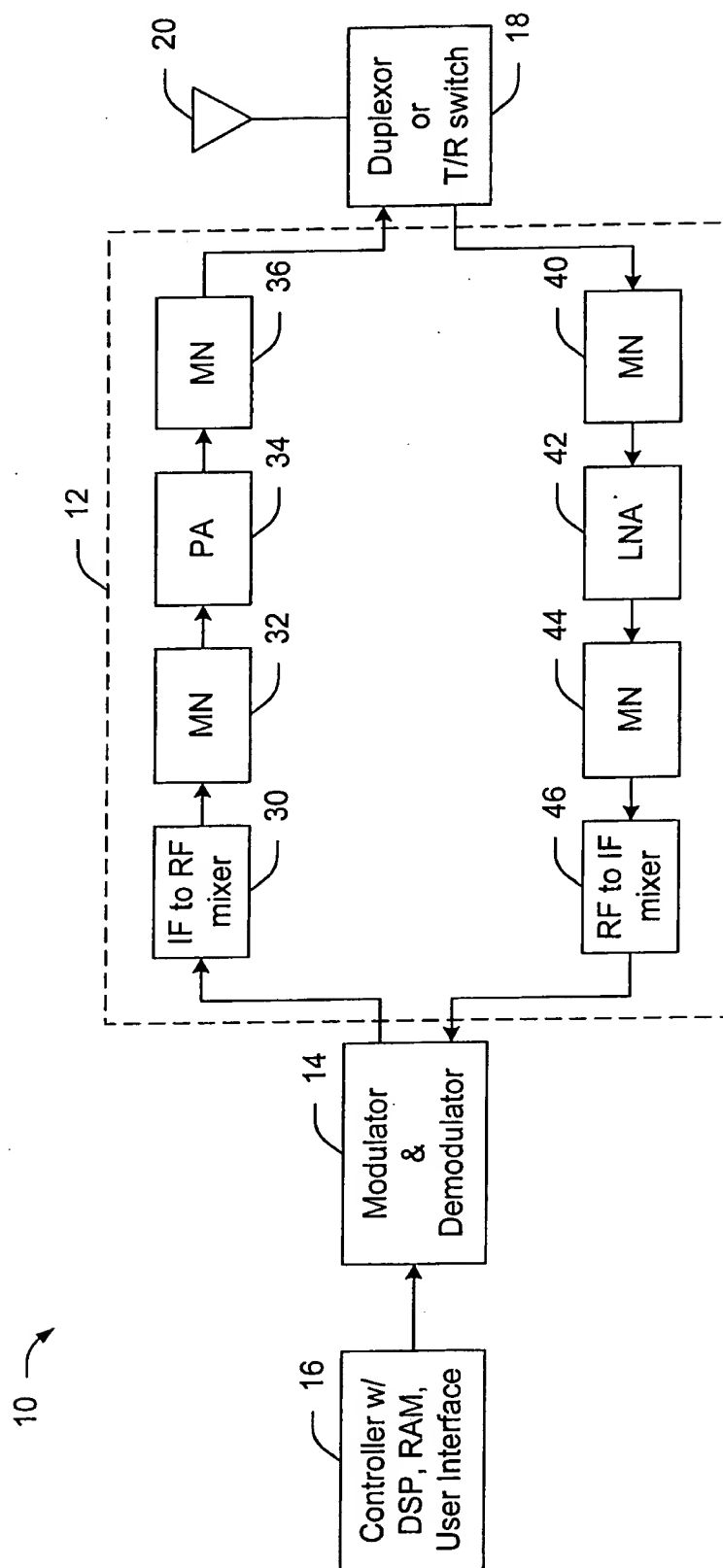
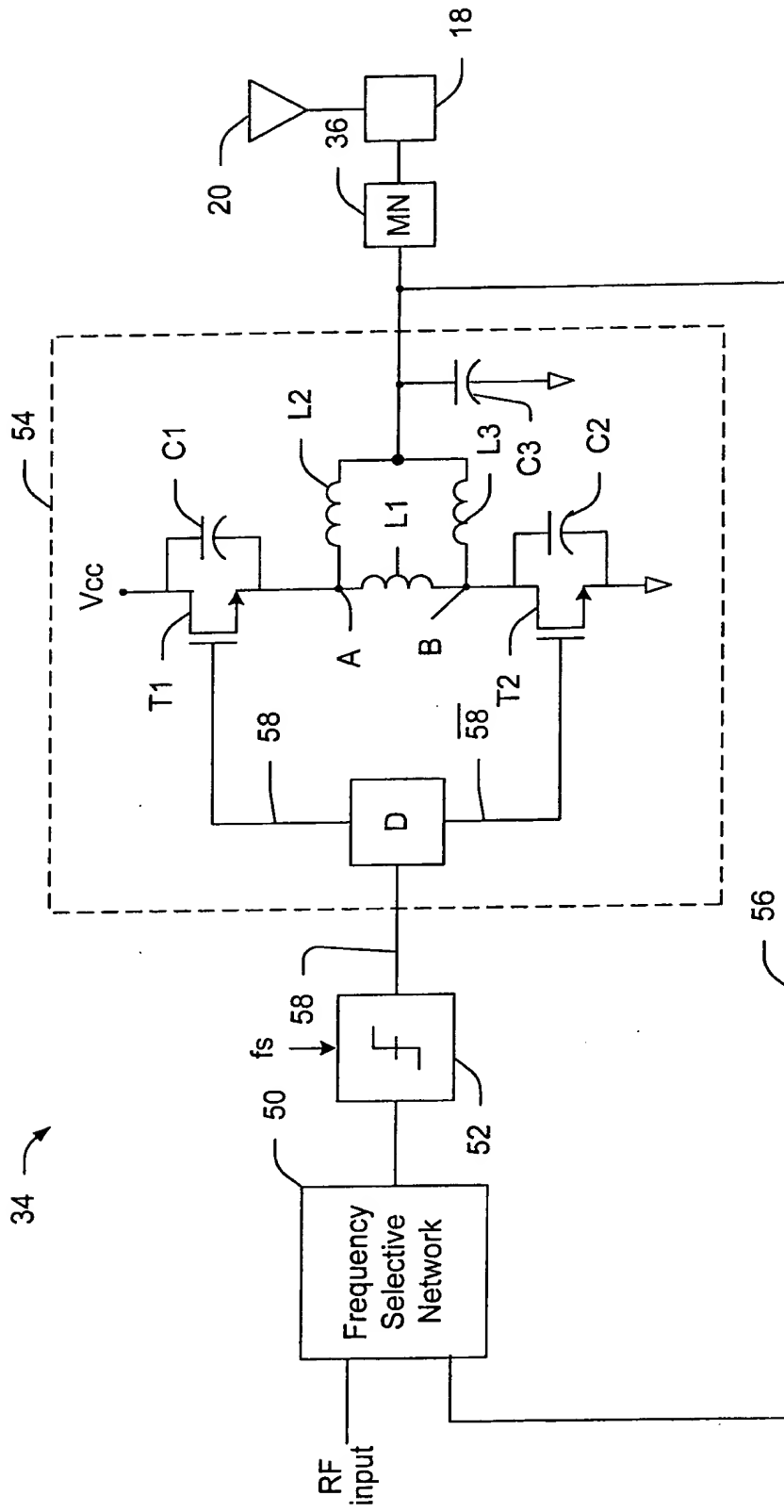


Fig. 1



**Fig. 2**

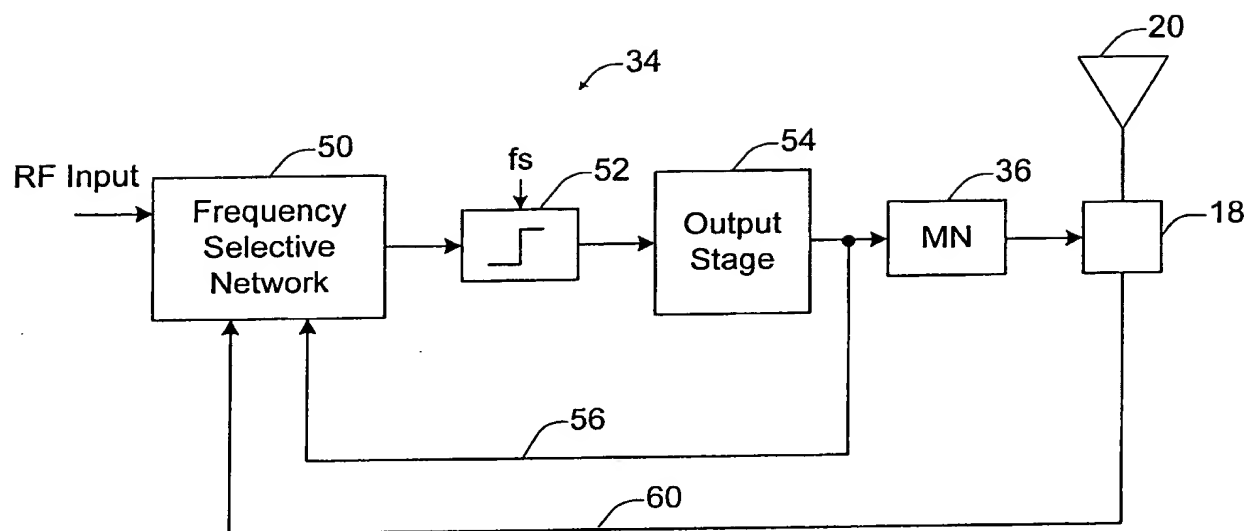


Fig. 3

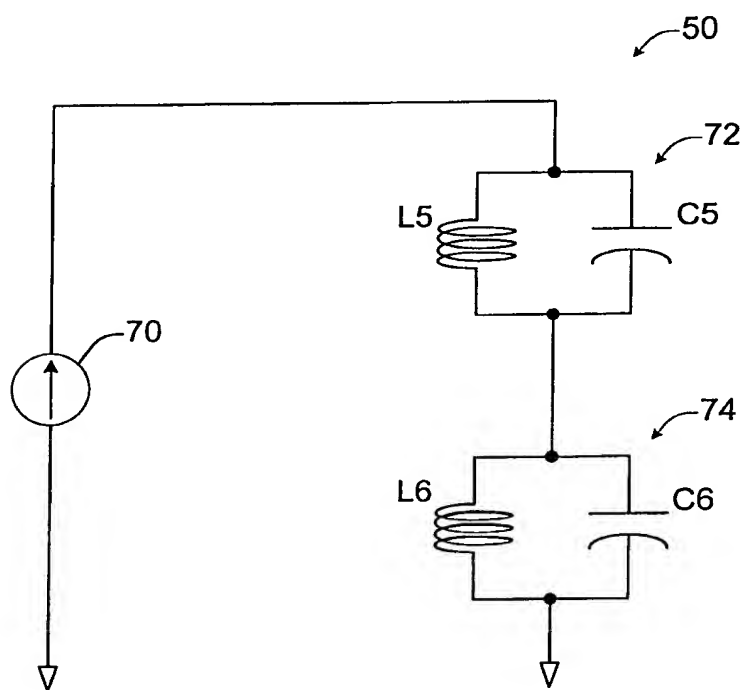


Fig. 4



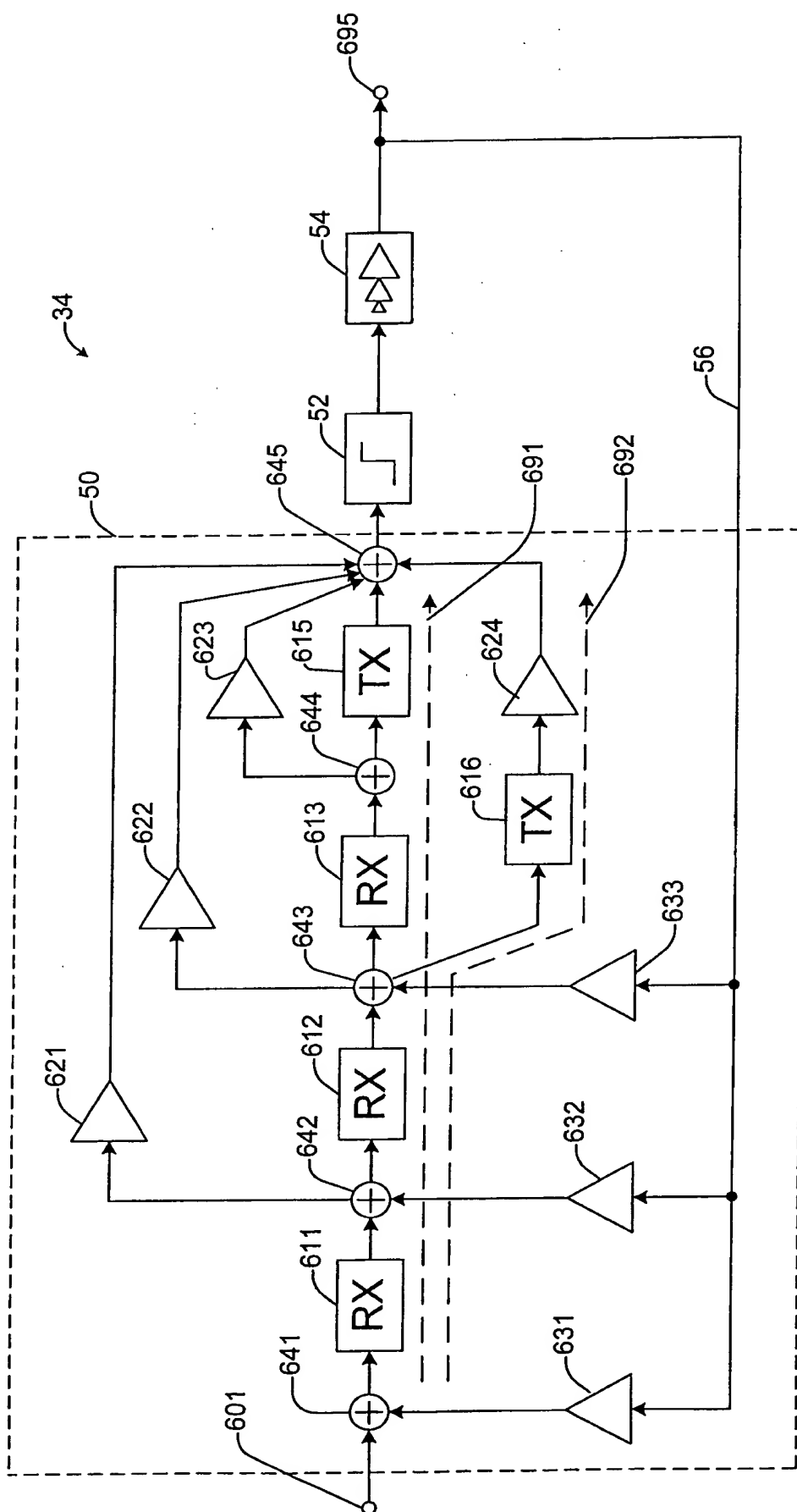
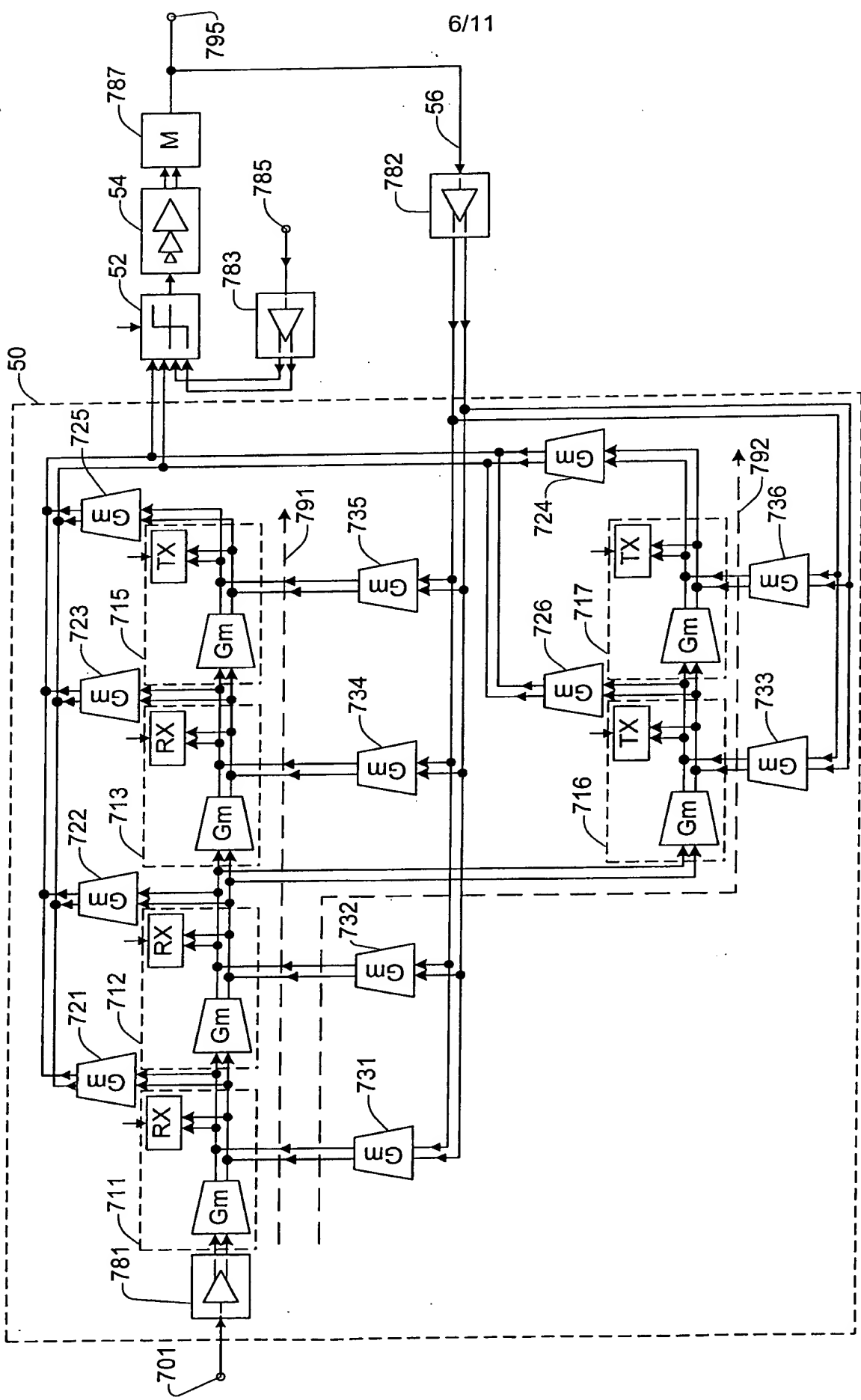


Fig. 6

FIG. 7 is a block diagram of a system 34, in accordance with an embodiment of the present invention. The system 34 includes a receiver 50 and a transmitter 56. The receiver 50 includes a first receiver 701 and a second receiver 702. The transmitter 56 includes a first transmitter 703 and a second transmitter 704. The system 34 also includes a multiplexer 54 and a demultiplexer 55. The system 34 is configured to receive and transmit signals over a communication channel.

34



6/11

Fig. 7

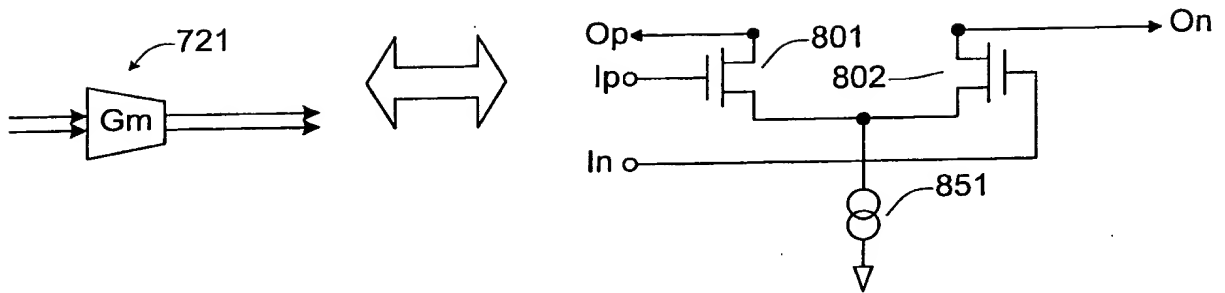


Fig. 8

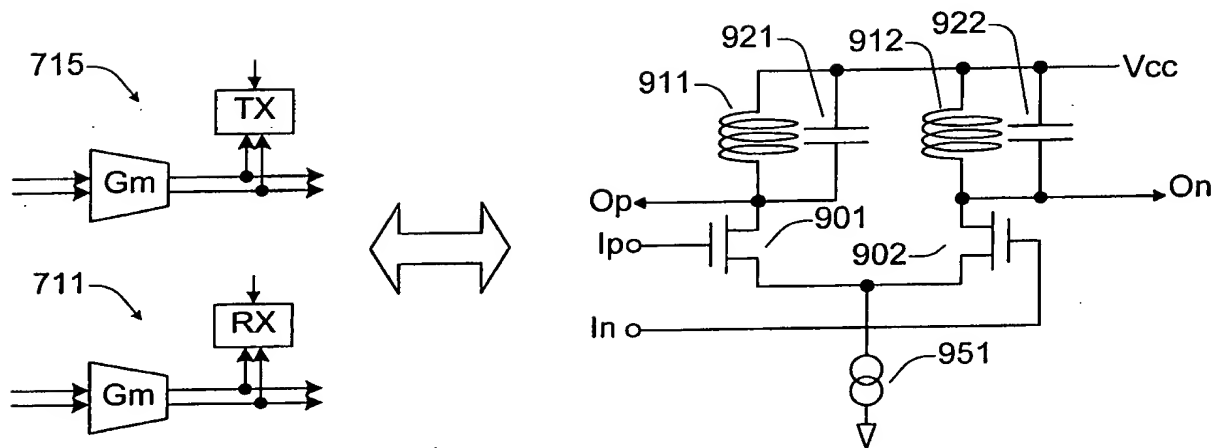
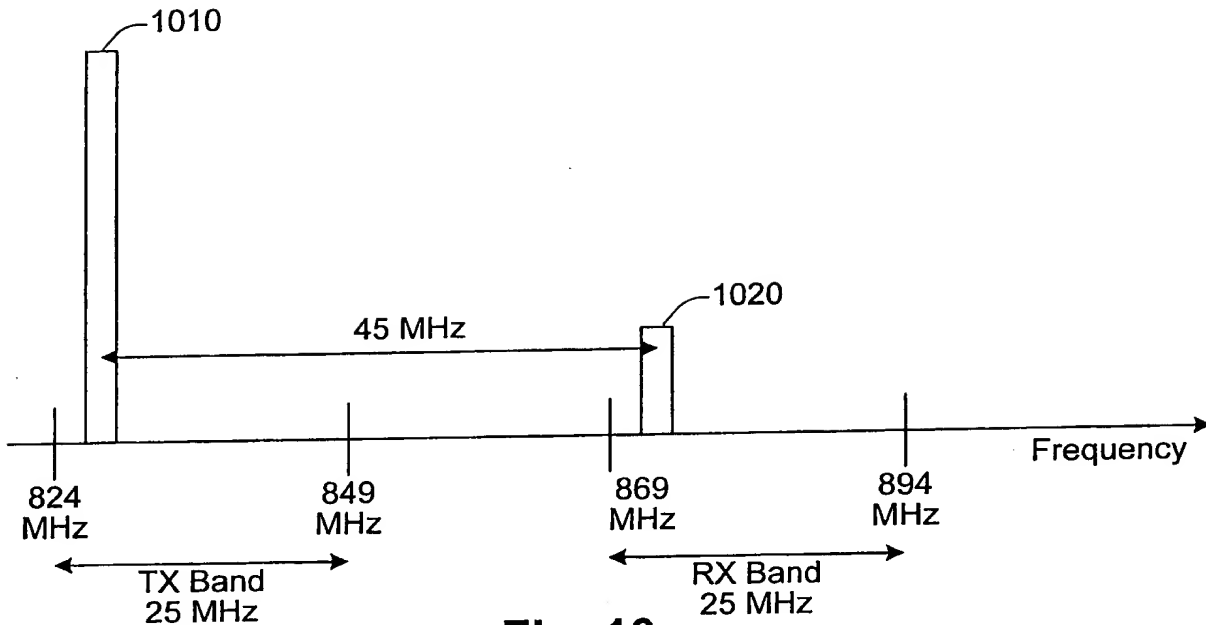
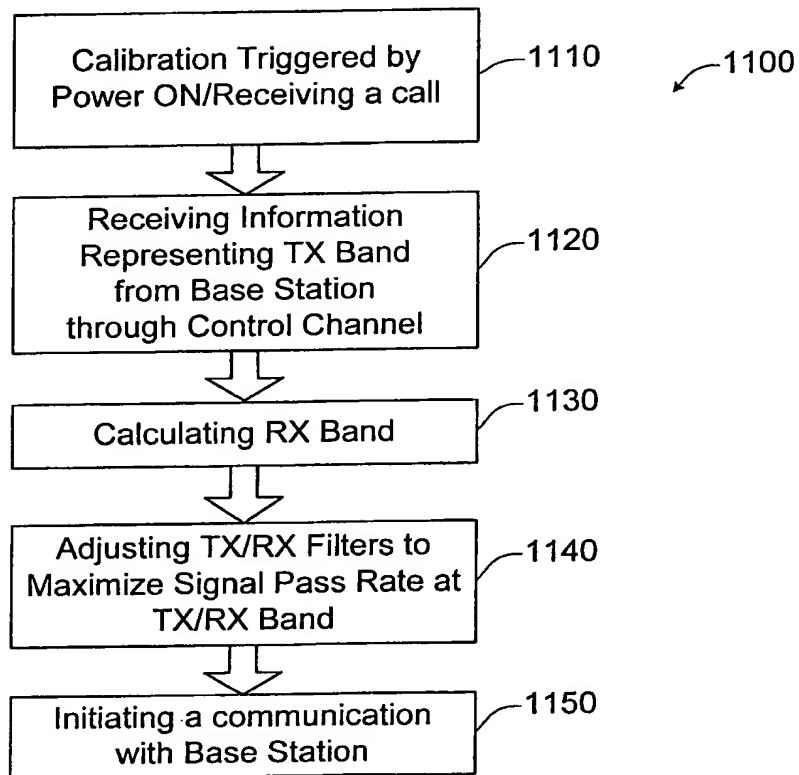
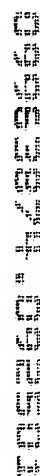


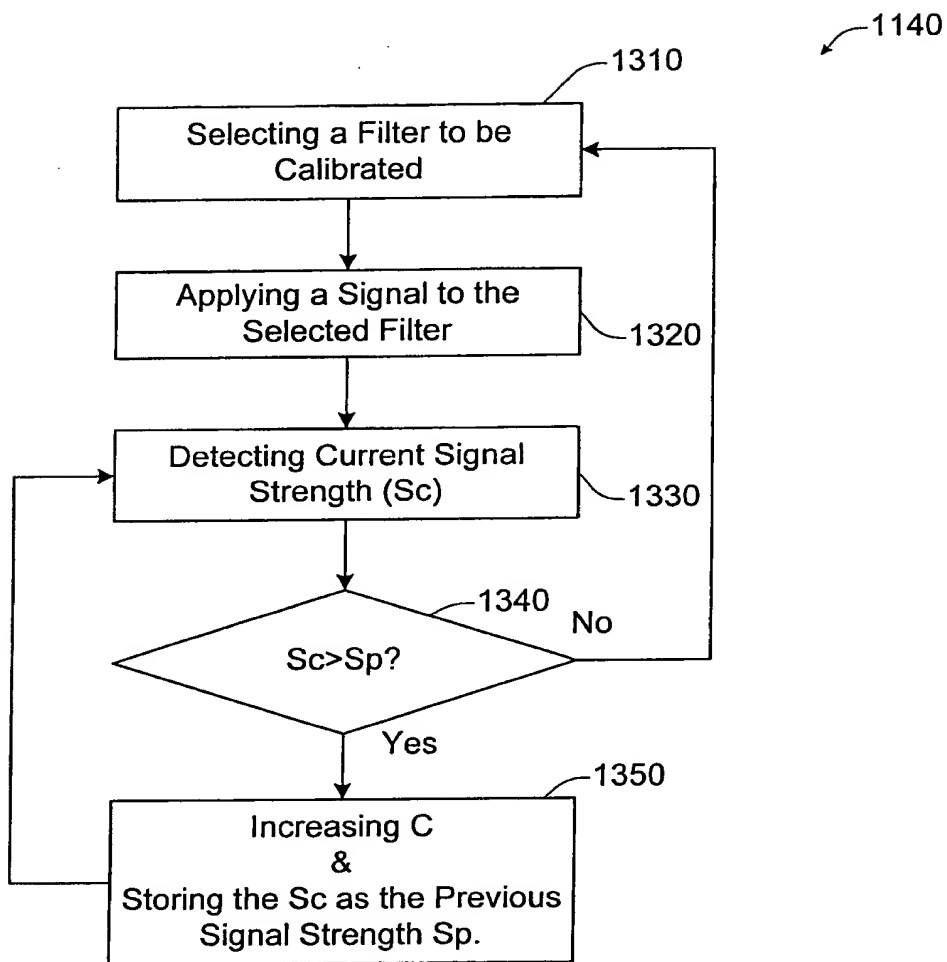
Fig. 9



**Fig. 10****Fig. 11**



1200

**Fig. 13**

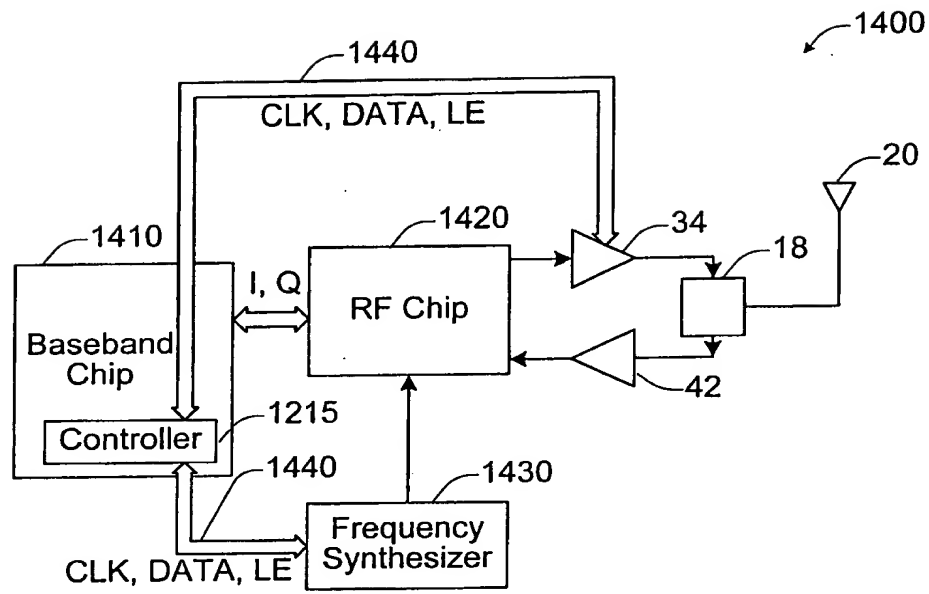


Fig. 14

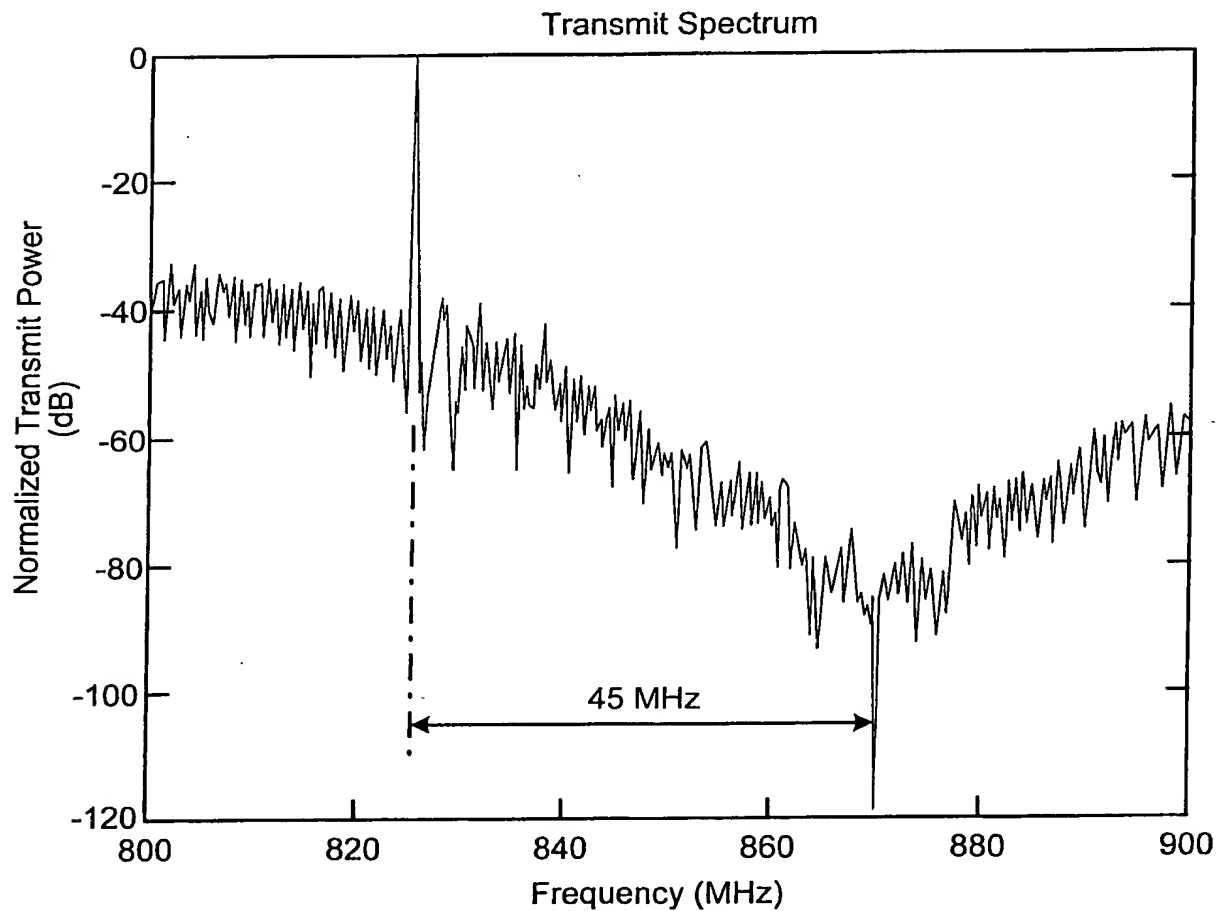


Fig. 15